

FIG. 1 (PRIOR ART)

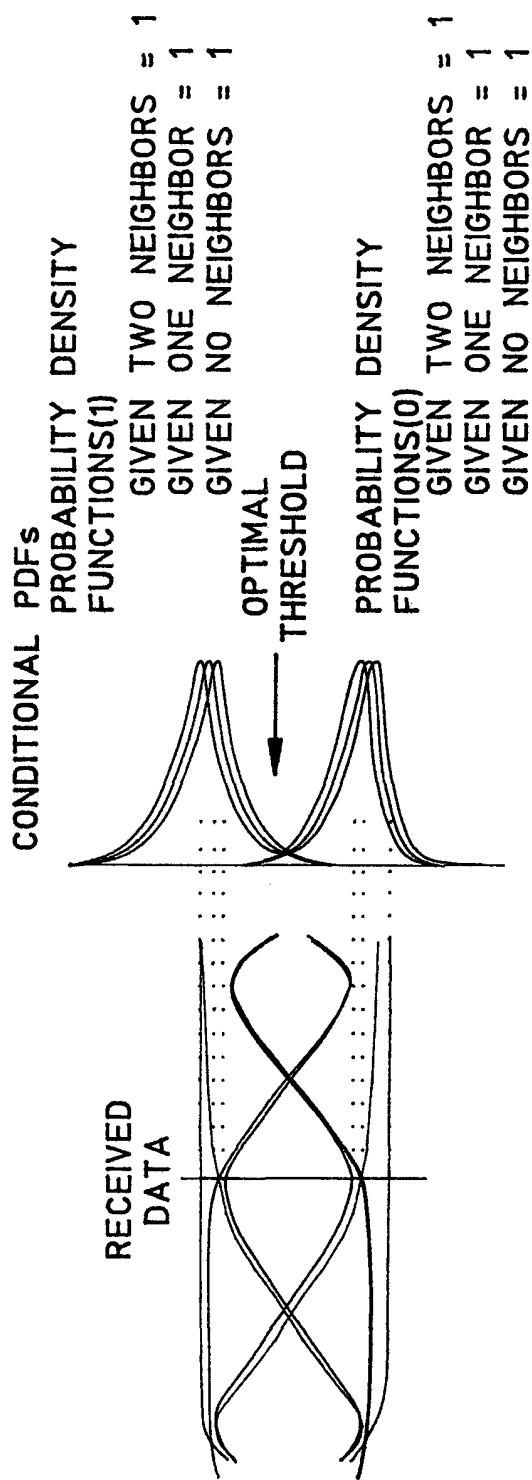


FIG. 2 (PRIOR ART)

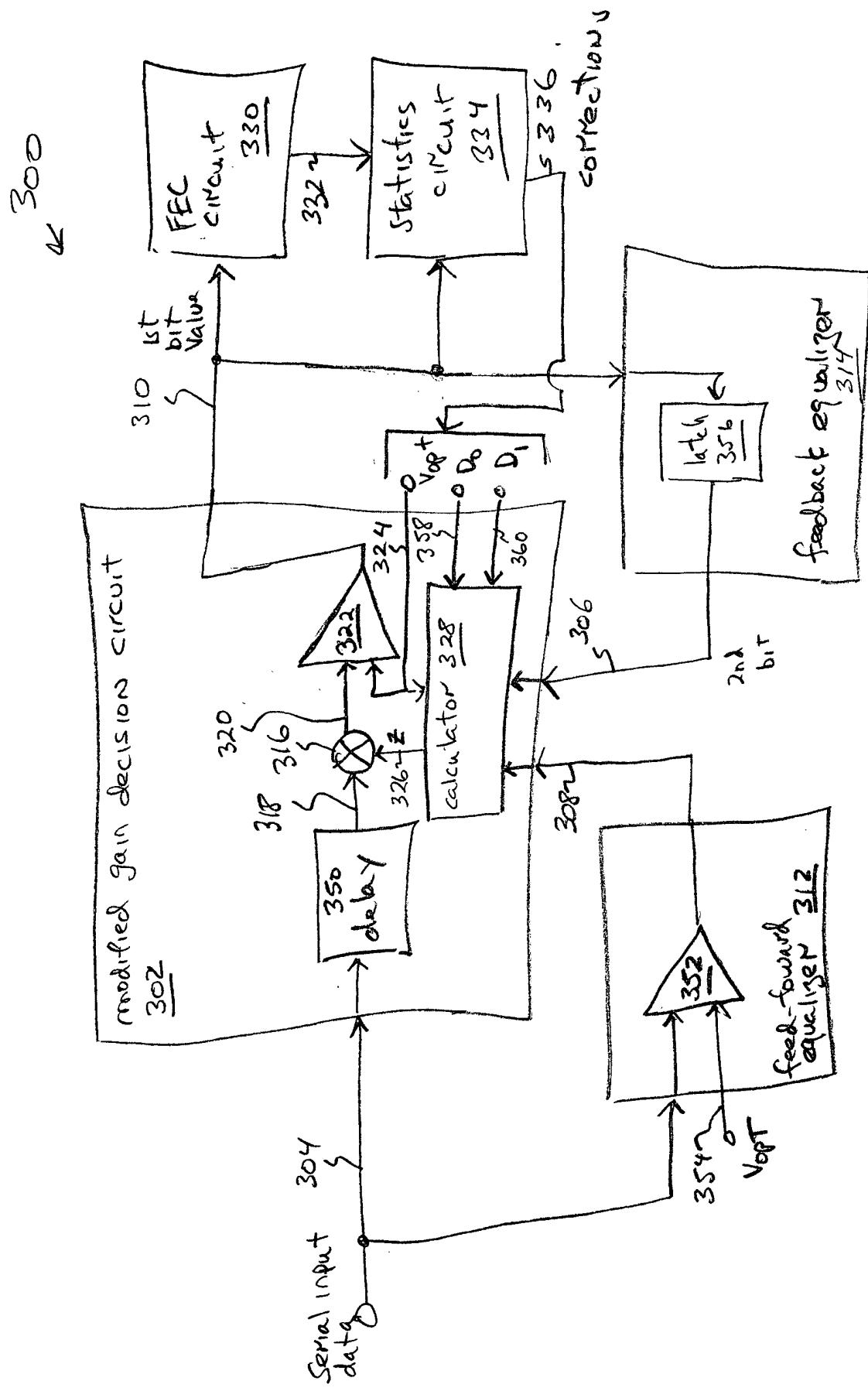


Fig. 3

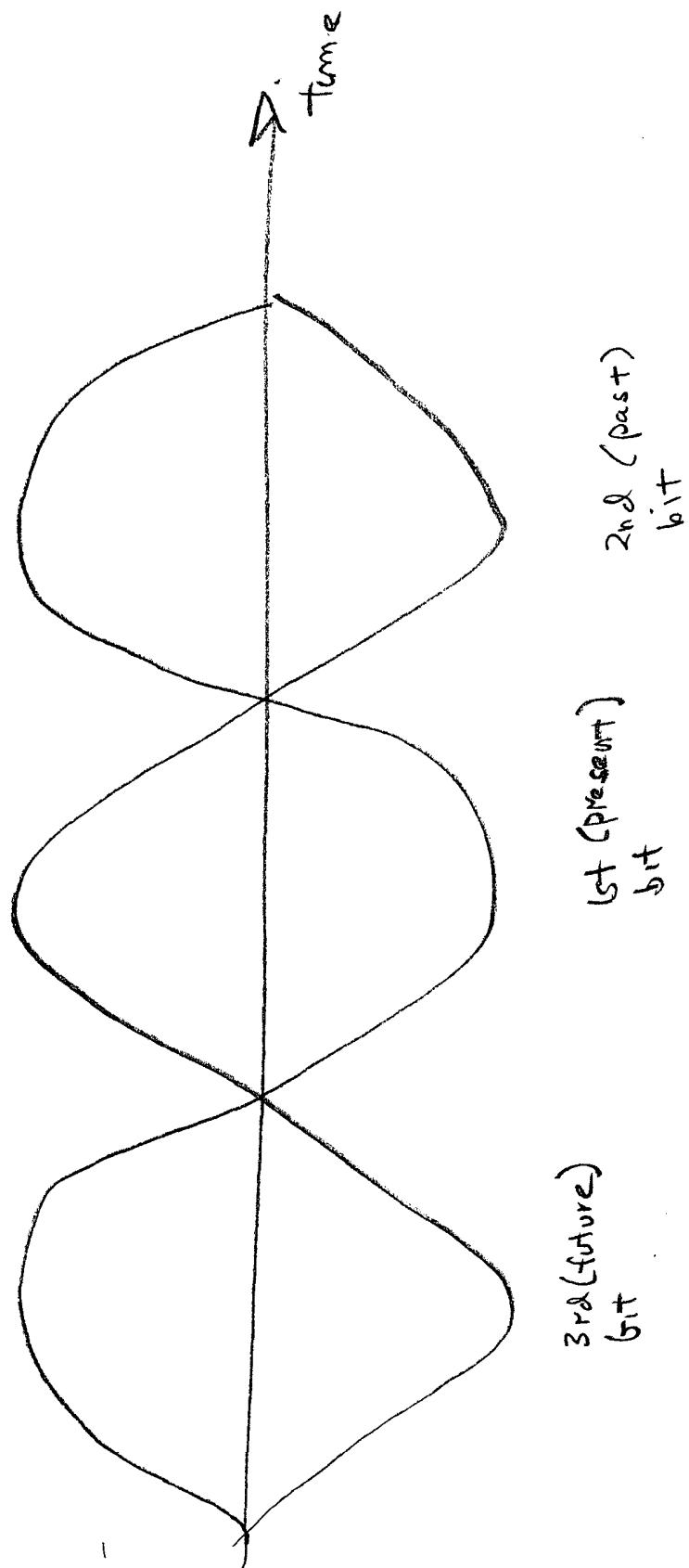


Fig. 4

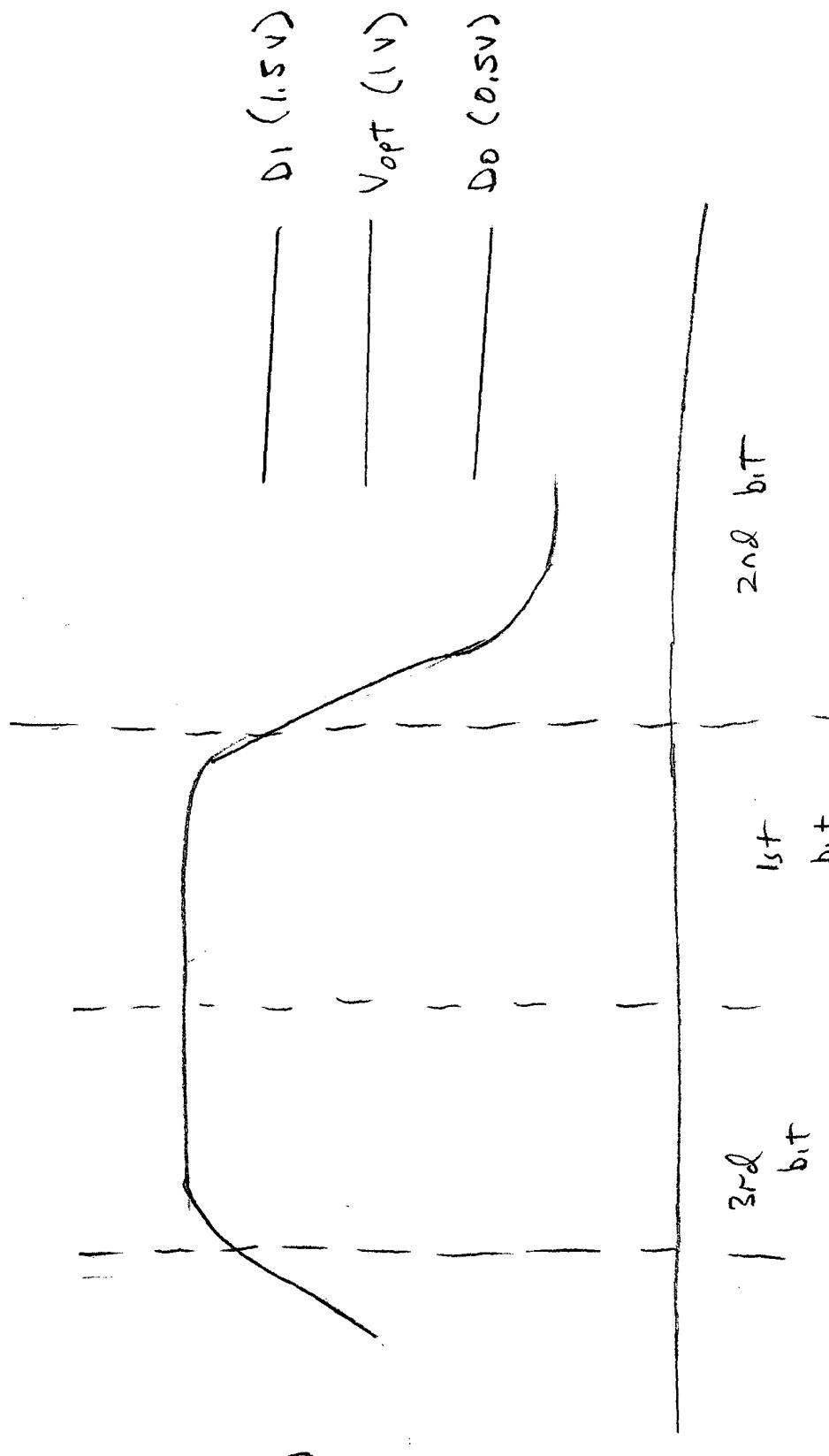


Fig. 5

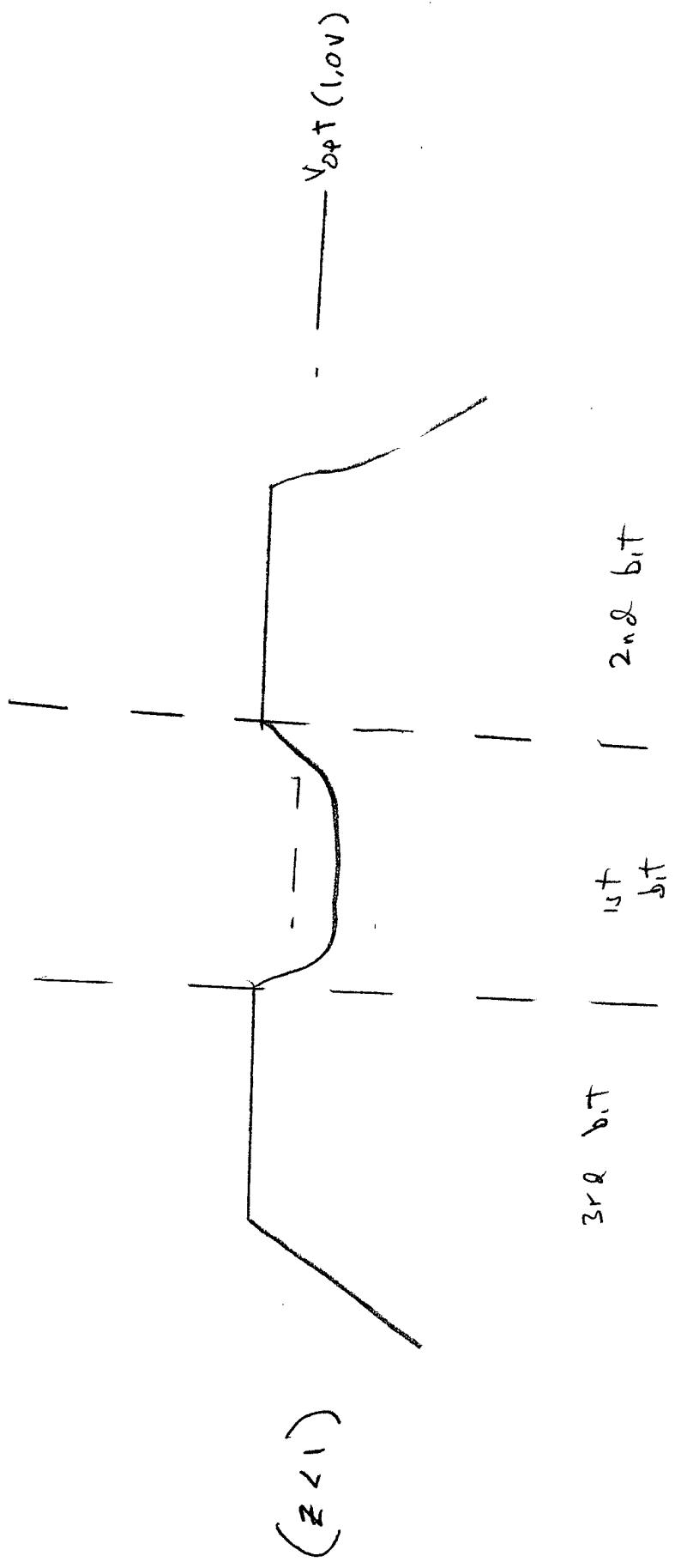


Fig. 6

INPUT LEVEL

D1

DEFINITE "1"

"0" IF BOTH PAST & FUTURE BIT VALUES = "1"  
"1" OTHERWISE

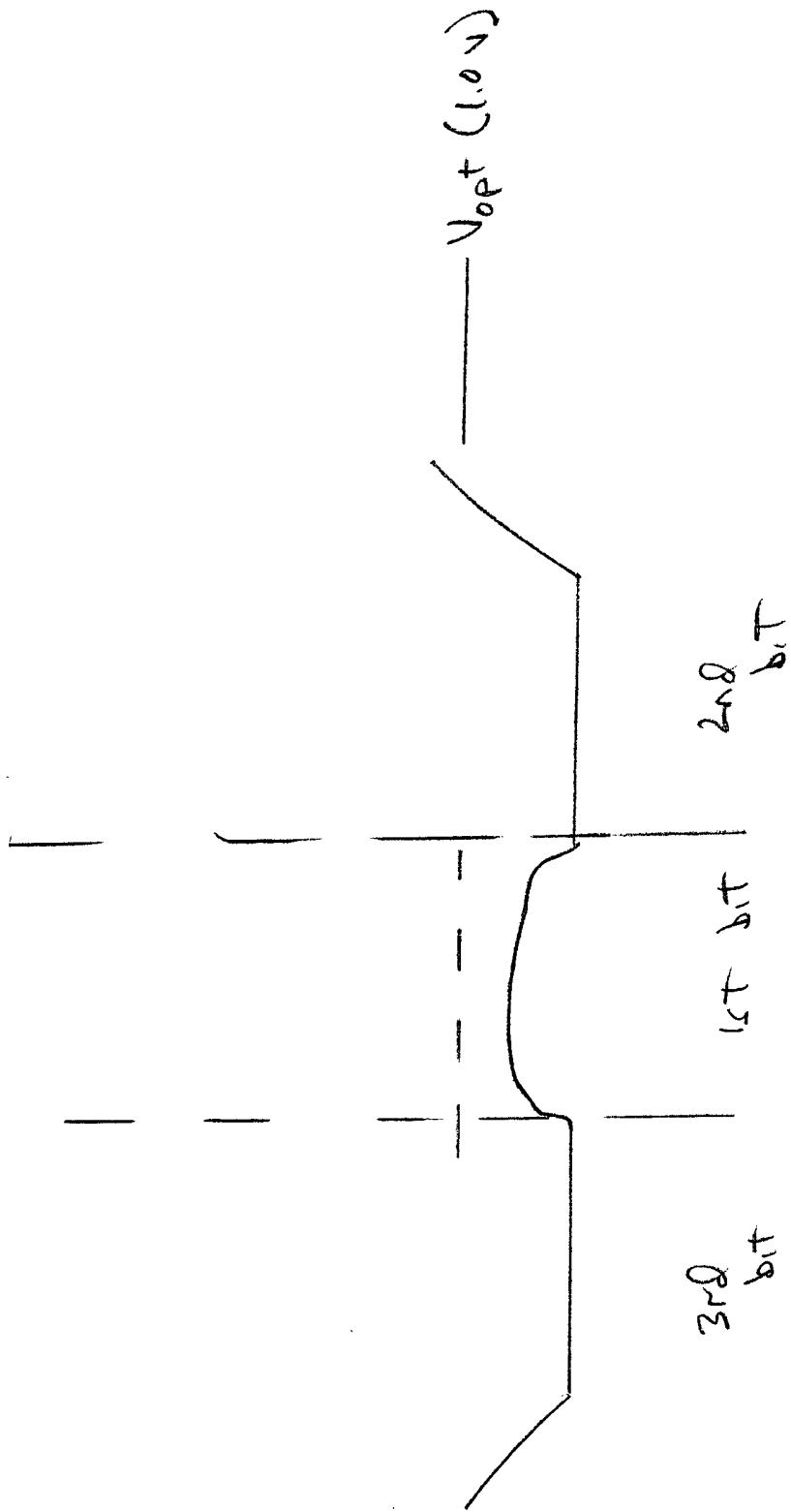
Vopt

"1" IF BOTH PAST & FUTURE BIT VALUES = "0"  
"0" OTHERWISE

Do

DEFINITE "0"

Fig. 7



$(z > 1)$

Fig. 8

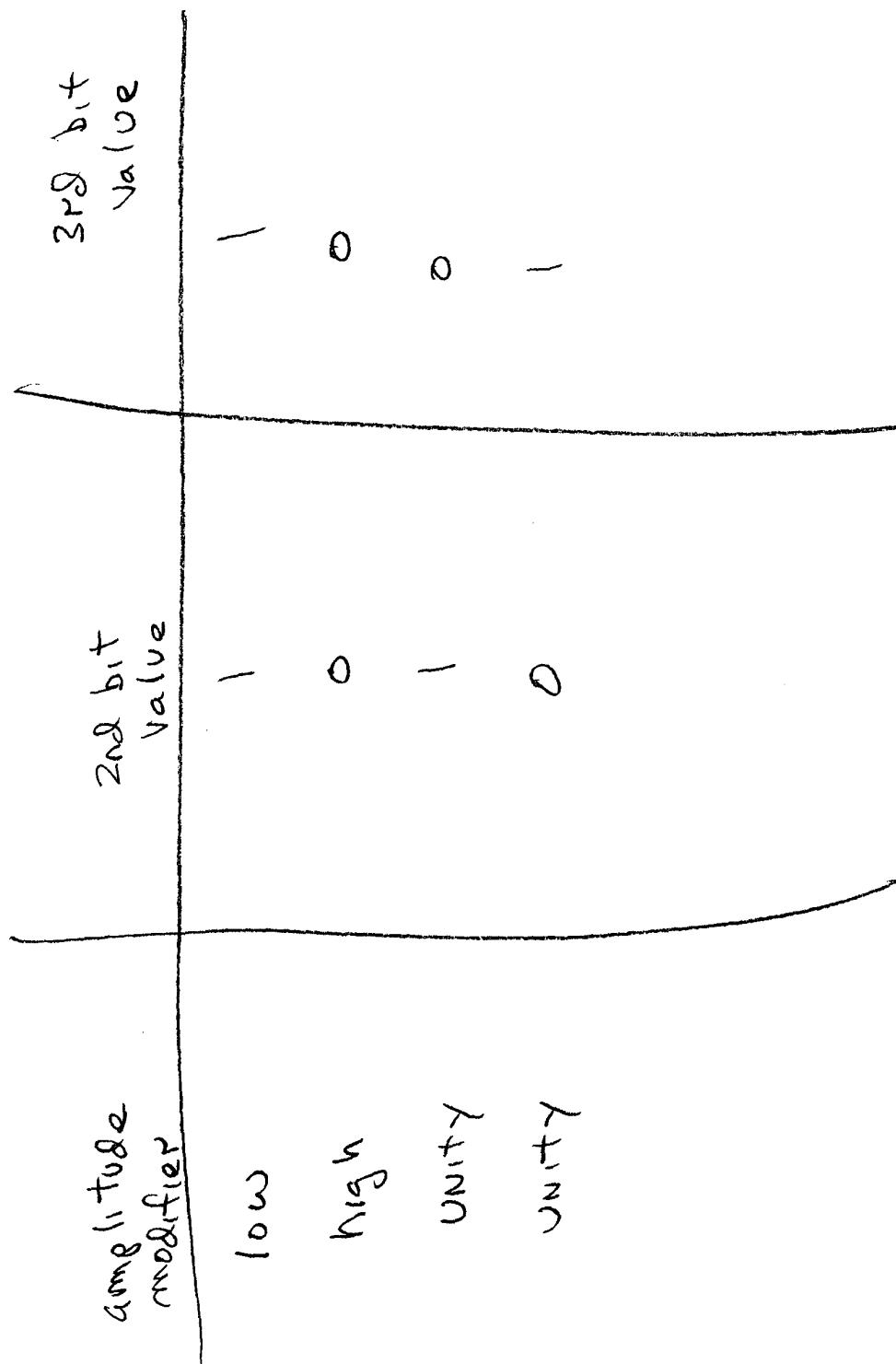


Fig. 9

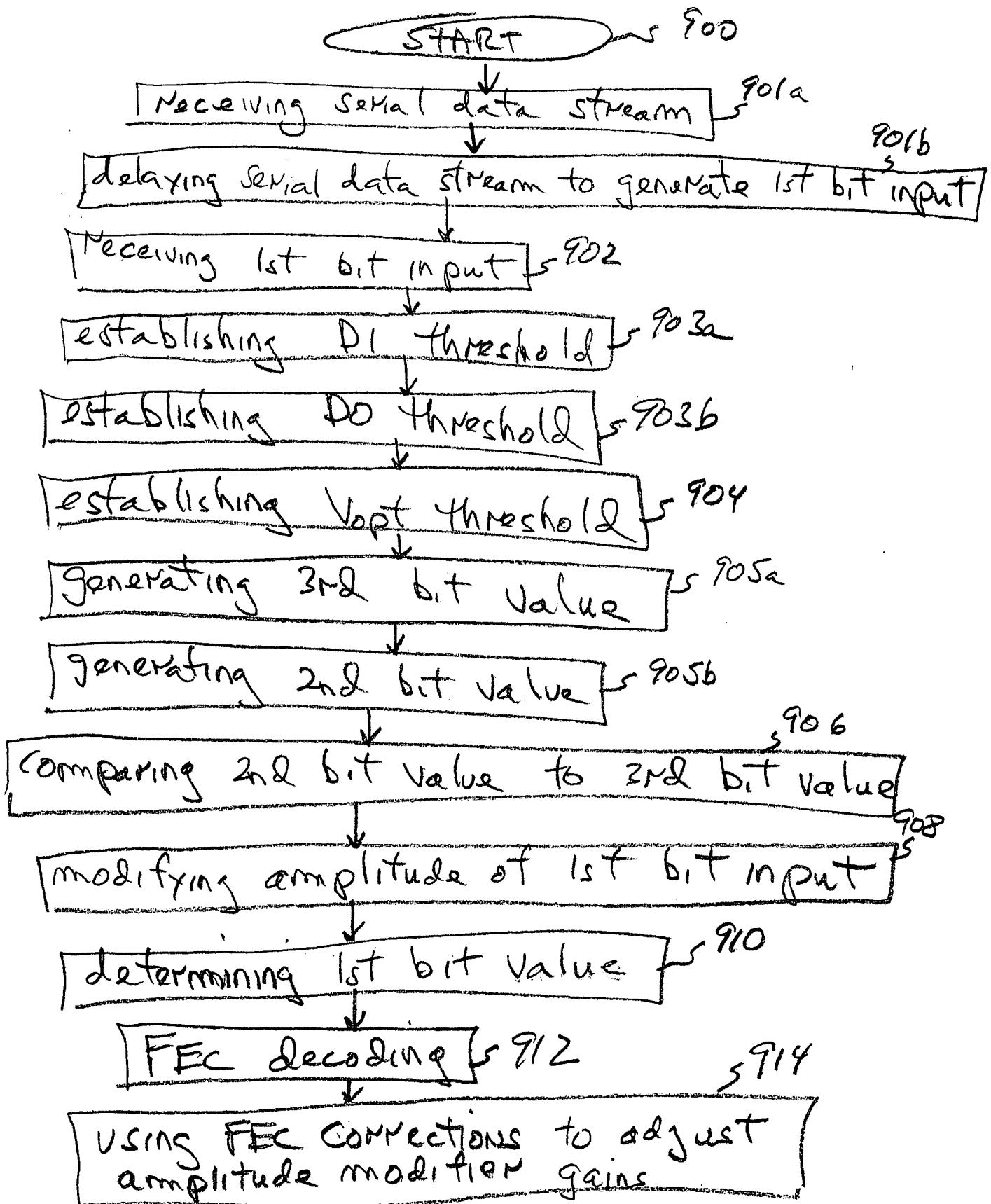


Fig. 10